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CLAIMS

What is claimed is:

 A computer-readable medium having stored thereon a data structure comprising:

a first field containing data representing an assigned target; and

a second field containing data representing a logic value to be assigned to said assigned target.

- 2. The computer-readable medium of **claim 1**, wherein said first field and said second field are incorporated within a comment line of a design target entity source code file.
- 3. The computer-readable medium of **claim 2**, further comprising a pre-pended comment identifier field for identifying said data structure as a hardware description language comment.
- 4. The computer-readable medium of **claim 1**, wherein said assigned target is a simulation event, and wherein said first field further contains data declaring said simulation event.
- 5. The computer-readable medium of **claim 4**, wherein said first field further comprises an event type subfield containing data for declaring a type of simulation event.

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- 6. The computer-readable medium of **claim 1**, wherein said assigned target is an intermediate signal, and wherein said first field further contains data declaring said intermediate signal.
- 7. The computer-readable medium of **claim 1**, wherein said assigned logic value is a logical connectivity expression for combining a plurality of signals.
- 8. The computer-readable medium of **claim 7**, wherein said assigned target is an intermediate signal, and wherein at least one of said plurality of signals originates as said intermediate signal.
- 9. The computer-readable medium of **claim 1**, wherein said assigned logic value is a logic function for combining at least two signals.